

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	35	phase adj lock\$3 adj loop\$4 with mosfet\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/12/27 17:55
S2	127	phase adj lock\$3 adj loop\$4 same mosfet\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/12/27 17:55

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	68	racing with flip adj1 flop	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:02
S2	7	racing with flip adj1 flop and synchroni\$7 with pulses	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/25 19:08
S3	2	rac\$3 with flip adj1 flop with synchroni\$7 with pulses	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/25 19:09
S4	3156	synchron\$7 with data with flip adj1 flop	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/25 19:09
S5	301	synchron\$7 with data with flip adj1 flop and phase adj detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/25 19:10
S6	36	synchron\$7 with dela\$4 with data with flip adj1 flop and phase adj detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/25 19:10
S7	264	inductor\$3 with flip adj1 flop	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:02
S8	19	inductor\$3 with flip adj1 flop and phase adj detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:07
S9	5	inductor\$3 with load same flip adj1 flop and phase adj detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:08
S10	19	inductor\$3 with flip adj1 flop and phase adj1 detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:09
S11	23	inductor\$3 with flip adj1 flop and phase near1 detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:19
S12	2293	differential with flip adj1 flop	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:19

S13	220	differential with flip adj1 flop and phase adj detect\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:21
S14	12534	differential with flip adj1 flop and phase adj detect\$4 nad clock adj port	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:19
S15	7	differential with flip adj1 flop and phase adj detect\$4 and clock adj port	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:20
S16	95	differential with input with flip adj1 flop and phase adj detect\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:27
S17	21	differential with input with flip adj1 flop same phase adj detect\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:32
S18	1140	optical with phase adj detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:32
S19	28	optical with receiv\$3 with transmit\$3 with phase adj detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:33
S20	2	optical with receiv\$3 with transmit\$3 with phase adj detect\$3 and photo adj1 diode	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:34
S21	299	light\$4 with diode with transmit\$4 and photo adj2 diode with receiv\$3 and amplif\$4 with receiv\$3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:35
S22	21	light\$4 with diode with transmit\$4 and photo adj2 diode with receiv\$3 and amplif\$4 with receiv\$3 and phase adj detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 17:36
S23	14	light\$4 adj3 diode with transmit\$4 and photo adj2 diode with receiv\$3 and amplif\$4 with receiv\$3 and phase adj detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 18:38
S24	14	light\$4 adj3 diode with transmit\$4 and photo adj2 diode with receiv\$3 and amplif\$4 with receiv\$3 and phase adj detect\$3 and optical	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/06/26 18:39